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Response Under 37 C.F.R. §1.116
Expedited Procedure
Examining Group 2814**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Jeffrey P. Gambino, et al.

Examiner: Shrinivas H. Rao

Serial No: 10/695,335

Art Unit: 2814

Filed: October 28, 2003

Docket: BUR920010040US2 (14574A)

For: FIN FIELD EFFECT TRANSISTOR
WITH SELF-ALIGNED GATE

Dated: August 28, 2006

Confirmation No: 4853

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE UNDER 37 C.F.R. § 1.116**

Sir/Madam:

In response to the Final Office Action dated June 27, 2006, Applicants respectfully request that the following remarks be entered into the record of the above-identified patent application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this document is being filed in the United States Patent and Trademark Office on the date shown below via facsimile transmission to Mail Stop AF, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 at United States Patent and Trademark Office facsimile transmission number (571) 273-8300.

Dated: August 28, 2006


Yongzhi Yang